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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	<b>Application Number</b>	09/770,478	
	<b>Filing Date</b>	Jan 29, 2001	
	<b>First Named Inventor</b>	Del Signore, James R.	
	<b>Group Art Unit</b>	2816	
	<b>Examiner Name</b>	Quan Tra	
<b>Total Number of Pages in This Submission</b>	1	<b>Attorney Docket Number</b>	230P051

**ENCLOSURES (check all that apply)**

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**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

<b>Firm or Individual name</b>	George R. McGuire HANCOCK & ESTABROOK, LLP
<b>Signature</b>	
<b>Date</b>	April 9, 2002

**CERTIFICATE OF MAILING**

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In re the application of:

James R. Del Signore II et al.

Docket No. 230 P 051

Filed: January 29, 2001

Art Unit: 2816

Ser. No. 09/770,478

Examiner: Quan Tra

For: Current Inrush Limiting Circuit

Confirmation No. 6823

AMENDMENT

Hon. Commissioner of Patents  
And Trademark  
Washington D.C. 20231

Sir:

In response to the Official Action of January 9, 2002, please amend the  
application as follows:

Please amend claim 1 to read as follows:

- 1        1. An inrush circuit for electronic devices having high input capacitance, said inrush  
2        circuit comprising:  
3            a.        means for providing a voltage ramp;  
4            b.        means defining an output voltage;  
5            c.        an operational amplifier circuit having a reference input, said  
6            operational amplifier circuit receiving said voltage ramp at said  
7            reference input and comparing a divided sample of said output voltage

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